BLAUPUNKT

BMW MID E39

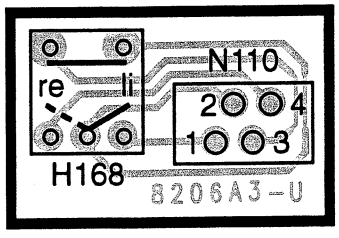
Multi **I**nformations **D**isplay

RADIO / UHR / DSP
7 608 727 040
RADIO / TEL / UHR / DSP
7 608 728 040
RADIO / BC / UHR / DSP
7 608 729 040
RADIO / BC / TEL / UHR / DSP
7 608 730 040
RADIO / UHR
7 608 731 040
RADIO / BC / TEL / UHR
7 608 732 040
RADIO / BC / UHR
7 608 733 040
RADIO / TEL / UHR
7 608 734 040

8 622 400 085 BN 07 / 94

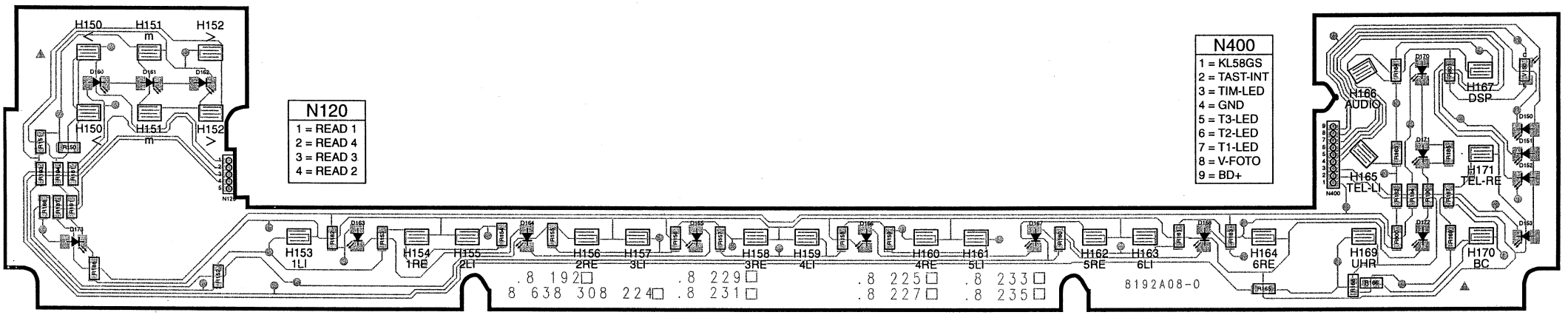
Schaltbild • Circuit diagramm • Schema du poste • Esquema del aparato

Geberplatte
Control board
PL 8206 A03 V00



N110
1 = D-LINKS
2 = GND
3 = READ 4
4 = D-RECHTS

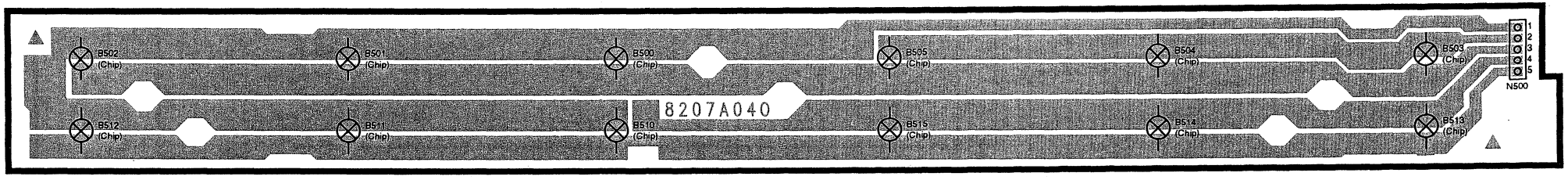
Schalterplatte
Key board
PL 8192 A08 V00
Chip

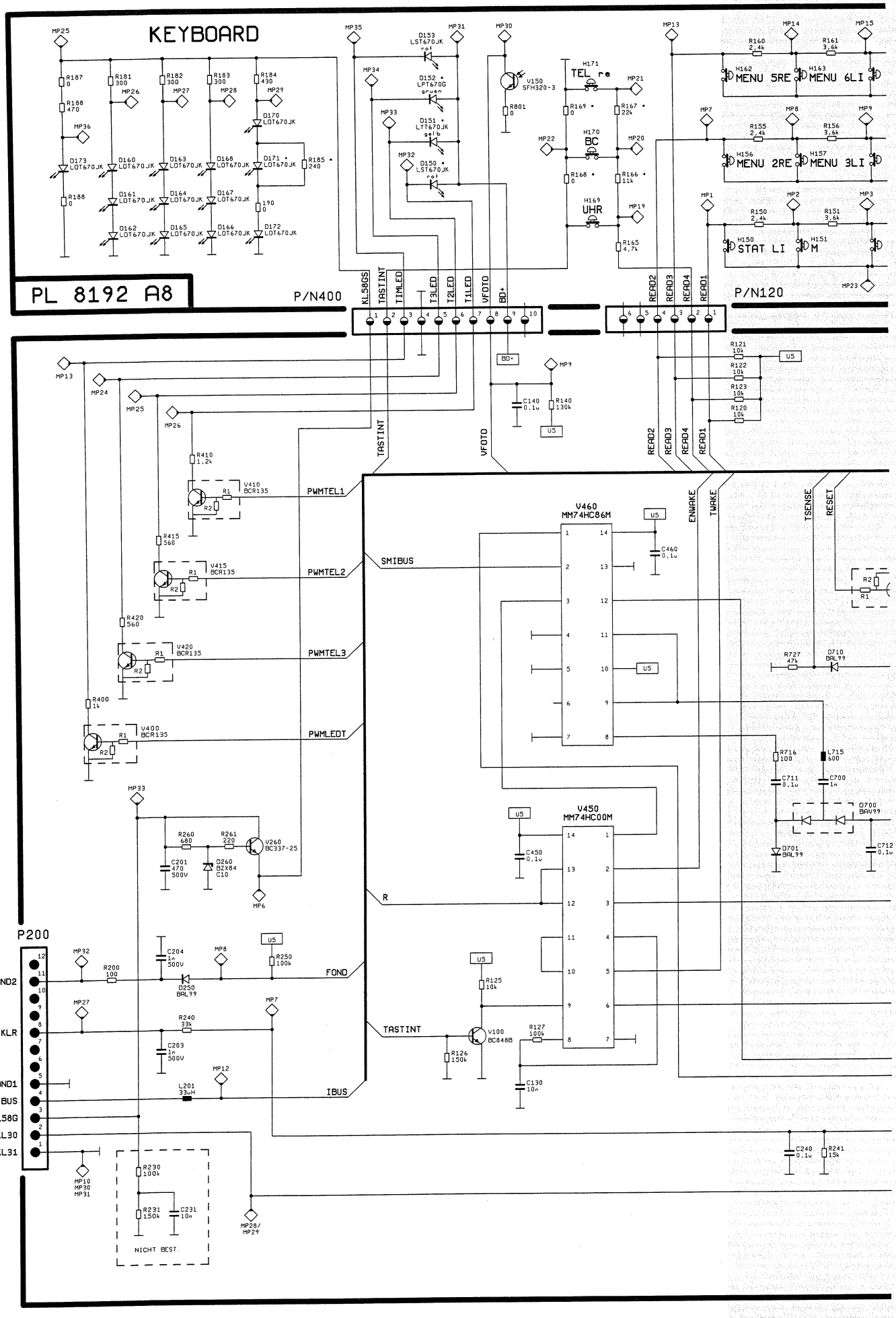


	H165 TEL-LI	H166 AUDIO	H167 DSP	H169 UHR	H170 BC	H171 TEL-RE
AUDIO/DSP/UHR 7 608 727 040	-	X	X	X	-	-
AUDIO/DSP/TEL/UHR 7 608 728 040	X	X	X	X	-	X
AUDIO/DSP/UHR/BC 7 608 729 040	-	X	X	X	X	-
AUDIO/DSP/TEL/UHR/BC 7 608 730 040	X	X	X	X	X	X
AUDIO/UHR 7 608 731 040	-	X	-	X	-	-
AUDIO/TEL/UHR/BC 7 608 732 040	X	X	-	X	X	X
AUDIO/UHR/BC 7 608 733 040	-	X	-	X	X	-
AUDIO/TEL/UHR 7 608 734 040	X	X	-	X	-	X

Lampenplatte
Lamp board
PL 8207 A04 V00
Chip

N500
1 = BD+
2 = LOUT1
3 = UM1
4 = LOUT2
4 = UM2




KEYBOARD
PL 8192 A8
P/N400
P/N120
P200
V460
MM74HC86M
V450
MM74HC00M
TEL re
BC
UHR
MENU 5RE
MENU 6LI
MENU 2RE
MENU 3LI
STAT LI
M
SFH320-3
PWMTEL1
PWMTEL2
PWMTEL3
PWMLEDT
SMIBUS
R
TASTINT
FOND
IBUS
FOND2
KLR
FOND1
KL58G
KL30
KL31
NICHT BEST.
KL58GS
TIMLED
T3LED
T2LED
T1LED
VFOTO
BD+
READ2
READ3
READ4
READ1
ENWAKE
TWAKE
TSENSE
RESET

GERAETEVARIANTE	BST.VARIANTE PLATTEN-NR.	D150 D151 D152	D171	R185	R164	R166	R167	R168	R169
AUDIO/DSP/UHR 7 608 727 040	..8 224	entf.	entf.	240	22k	0	entf.	0	entf.
AUDIO/DSP/TEL/UHR 7 608 728 040	..8 229	LED	LED	entf.	22k	0	33k	0	entf.
AUDIO/DSP/UHR/BC 7 608 729 040	..8 231	entf.	entf.	240	22k	11k	entf.	entf.	0
AUDIO/DSP/TEL/UHR/BC 7 608 730 040	..8 192	LED	LED	entf.	22k	11k	22k	entf.	0
AUDIO/UHR 7 608 731 040	..8 225	entf.	entf.	240	0	0	entf.	0	entf.
AUDIO/TEL/UHR/BC 7 608 732 040	..8 227	LED	LED	entf.	0	11k	22k	entf.	0
AUDIO/UHR/BC 7 608 733 040	..8 233	entf.	entf.	240	0	11k	entf.	entf.	0
AUDIO/TEL/UHR 7 608 734 040	..8 235	entf.	entf.	entf.	0	0	33k	0	entf.

• = VERSIONSABH. BEST.

P/N120

INKREMENTENGEBER-PLATTE
INCREMENTAL CONTROL BOARD

H168 (LI / RE)

P/N110

PL 8206 A3

LAMPENPLATTE
LAMP BOARD

B512 6,3V/100mA
B502 6,3V/100mA
B511 6,3V/100mA
B501 6,3V/100mA
B510 6,3V/100mA
B500 6,3V/100mA
B513 6,3V/100mA
B505 6,3V/100mA
B514 6,3V/100mA
B504 6,3V/100mA
B513 6,3V/100mA
B503 6,3V/100mA

BO+

P/N500

PL 8207 A4

GERAETEVARIANTE	R410 V410	R415 V415	R420 V420	BEST.VARIANTE PLATTEN-NR.
AUDIO/DSP/UHR 7 608 727 040	-	-	-	... 8 222
AUDIO/DSP/TEL/UHR 7 608 728 040	X	X	X	... 8 191
AUDIO/DSP/UHR/BC 7 608 729 040	-	-	-	... 8 222
AUDIO/DSP/TEL/UHR/BC 7 608 730 040	X	X	X	... 8 191
AUDIO/UHR 7 608 731 040	-	-	-	... 8 222
AUDIO/TEL/UHR/BC 7 608 732 040	X	X	X	... 8 191
AUDIO/UHR/BC 7 608 733 040	-	-	-	... 8 222
AUDIO/TEL/UHR 7 608 734 040	X	X	X	... 8 191

X = BESTUECKT - = NICHT BESTUECKT

Hauptplatte
Mainboard
PL 8191 A05 V00

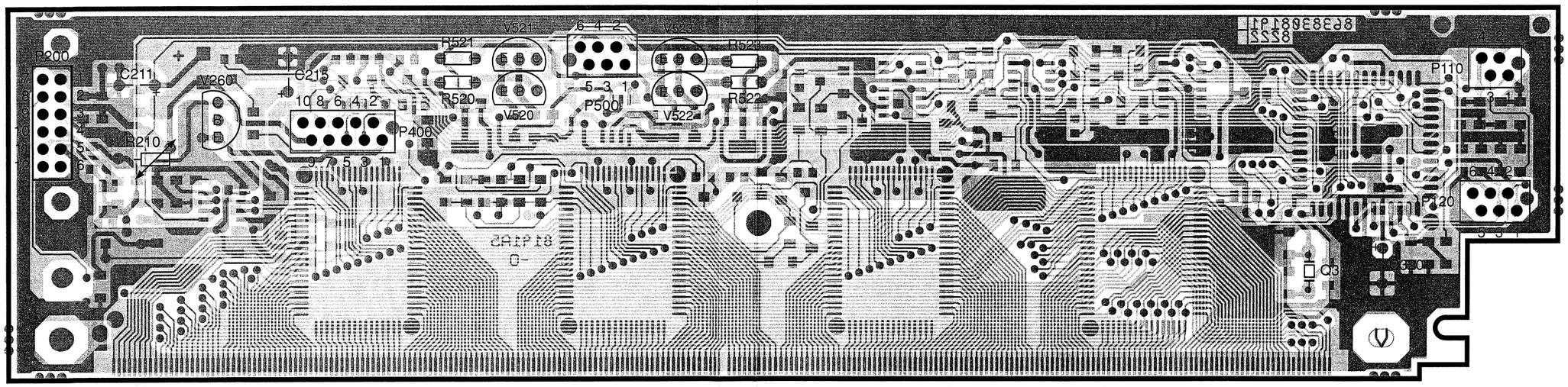
P200
1 = KL31
2 = KL30
3 = KL58G
4 = I-BUS
5 = FOND-1
6 =
7 =
8 = KLR
9 =
10 =
11 =
FOND-2

P400
1 = KL58GS
2 = TAST-INT
3 = TIM-LED
4 = GND
5 = T3-LED
6 = T2-LED
7 = T1-LED
8 = V-FOTO
9 = BD+

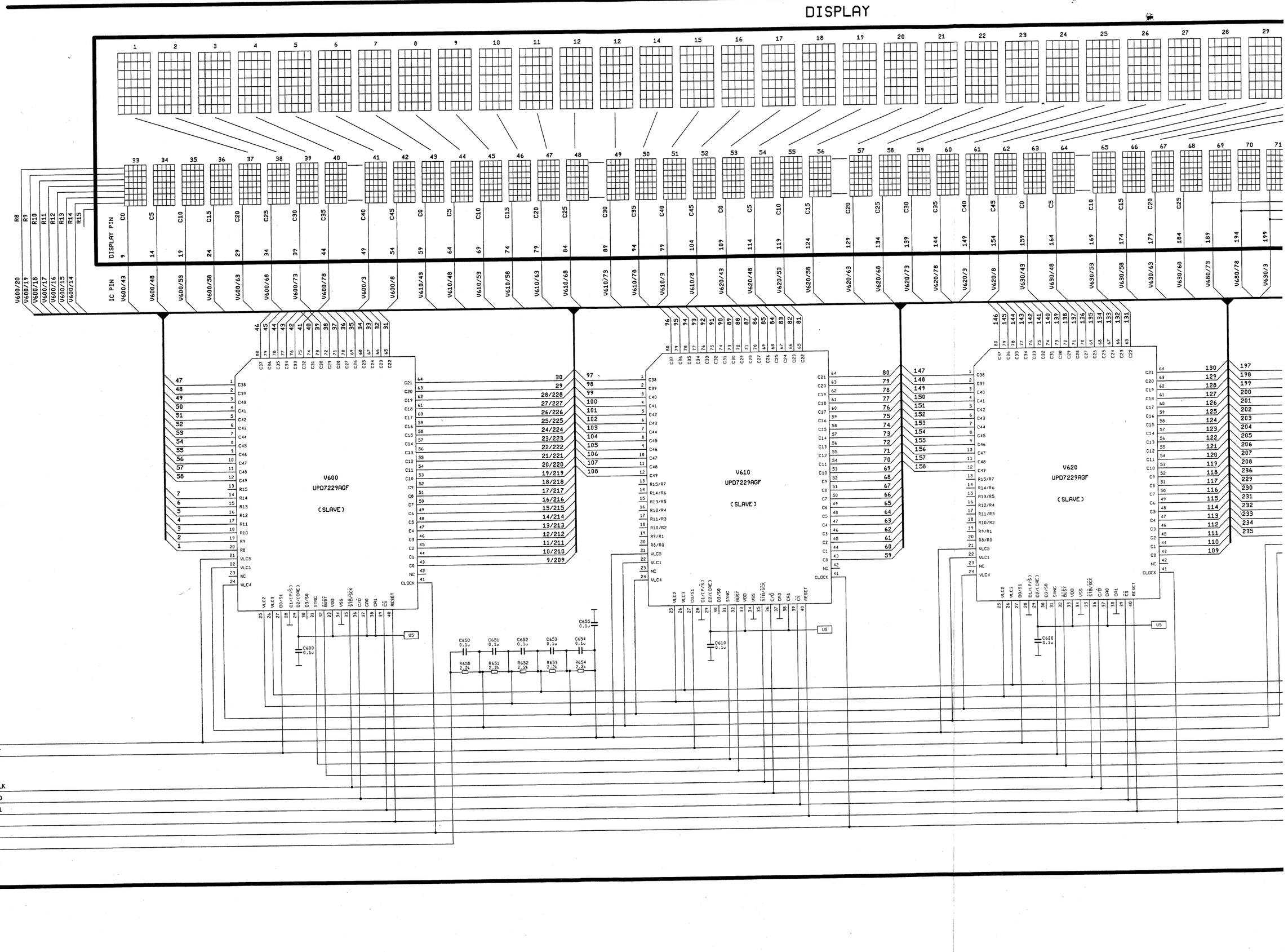
P500
1 = BD+
2 = L-OUT1
3 = UM1
4 = L-OUT2
4 = UM2

P120
1 = READ 1
2 = READ 4
3 = READ 3
4 = READ 2

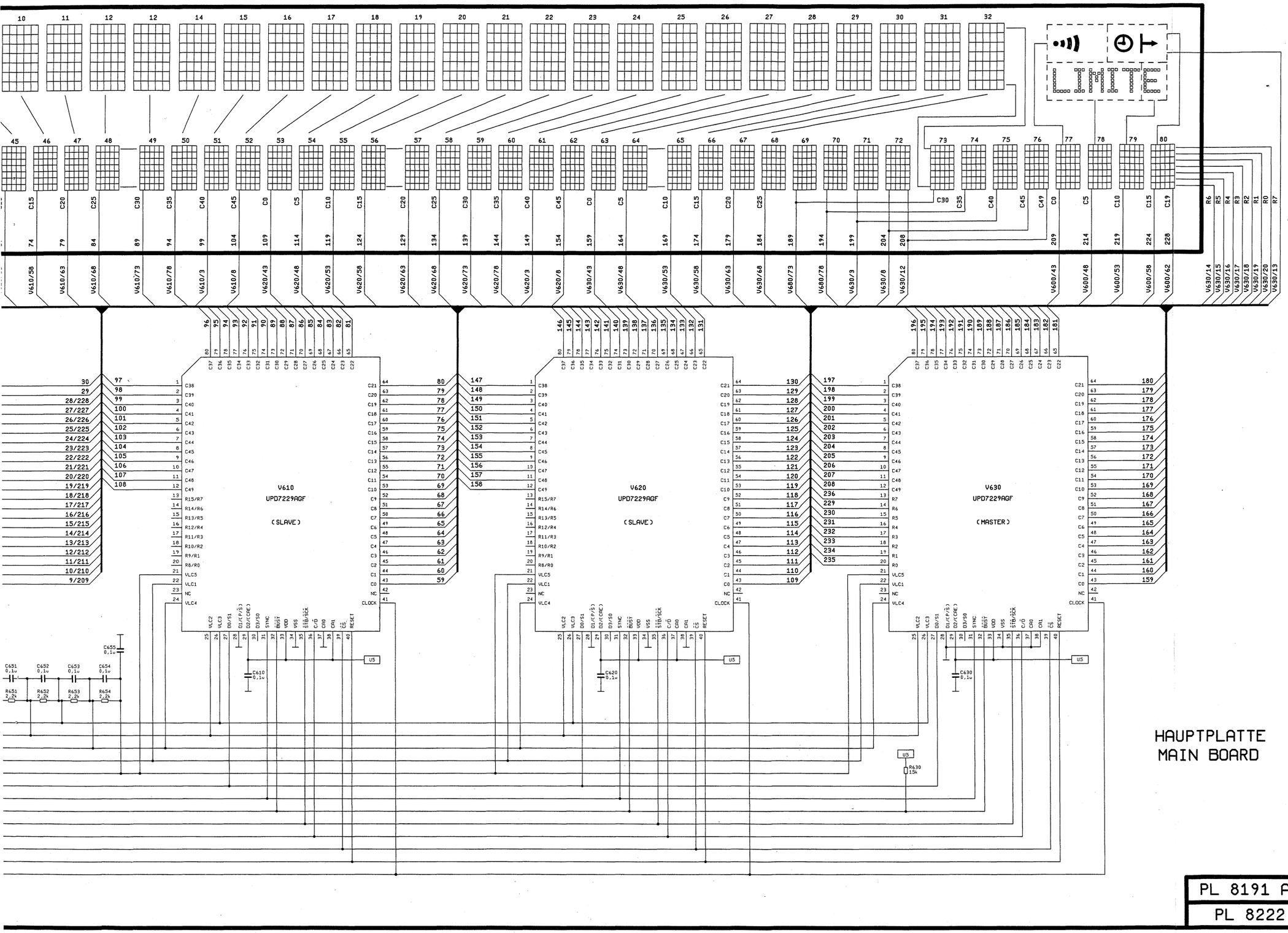
P110
1 = D-LINKS
2 = GND
3 = READ 4
4 = D-RECHTS



DISPLAY


DISPLAY PIN
IC PIN
V600
UPD7229AGF
(SLAVE)
V610
UPD7229AGF
(SLAVE)
V620
UPD7229AGF
(SLAVE)
U5
C600 0,1u
C610 0,1u
C620 0,1u
C650 0,1u
C651 0,1u
C652 0,1u
C653 0,1u
C654 0,1u
C655 0,1u
R650 2,2k
R651 2,2k
R652 2,2k
R653 2,2k
R654 2,2k
-VLCD
LCDDAT
LCDSCLK
LCDC/D
LCDCS1
RESET
CLK
+VLCD

DISPLAY

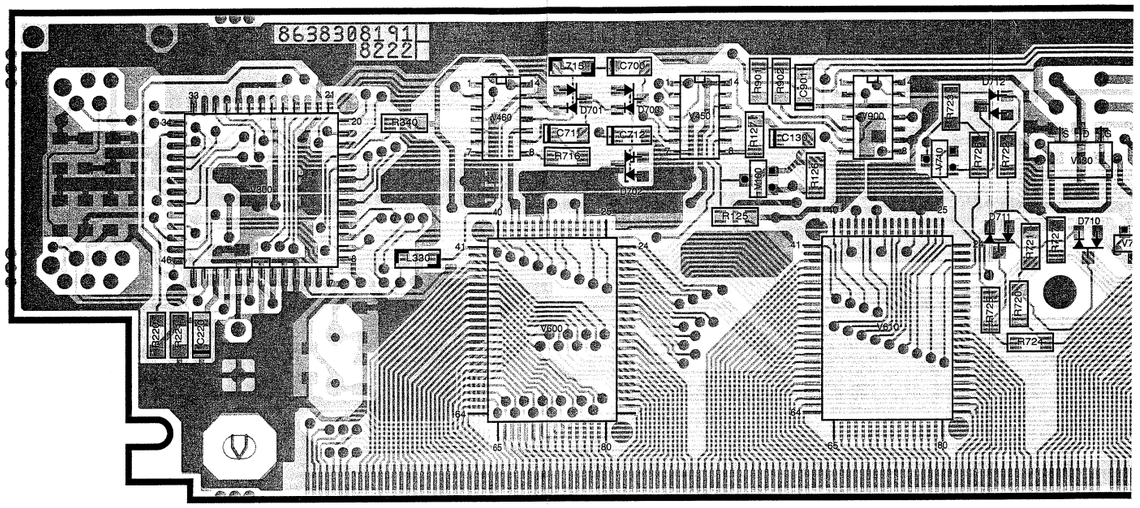


HAUPTPLATTE
MAIN BOARD

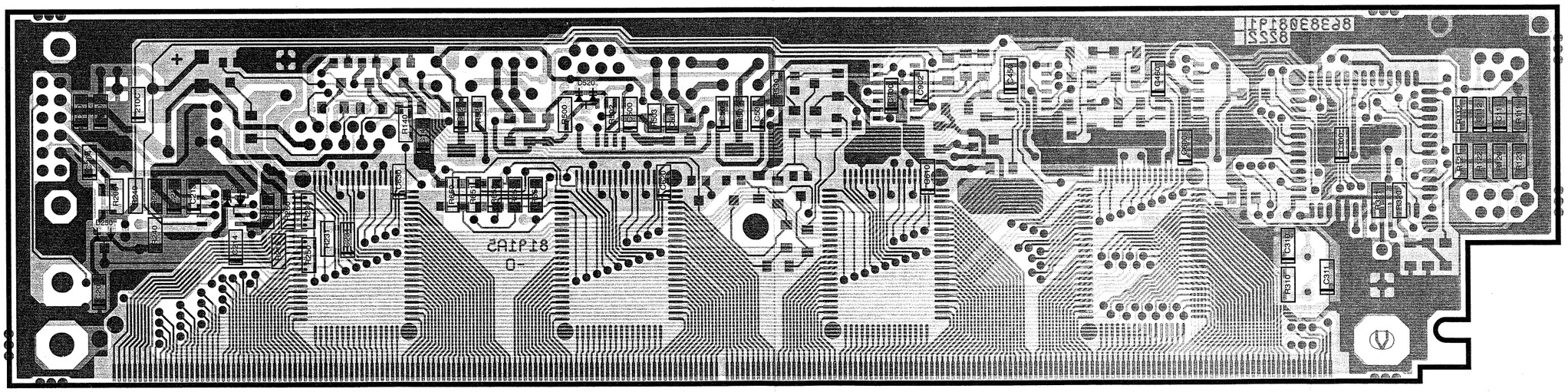
PL 8191 A5

PL 8222

Hauptplatte
Mainboard
PL 8191 A05 V00
Chip



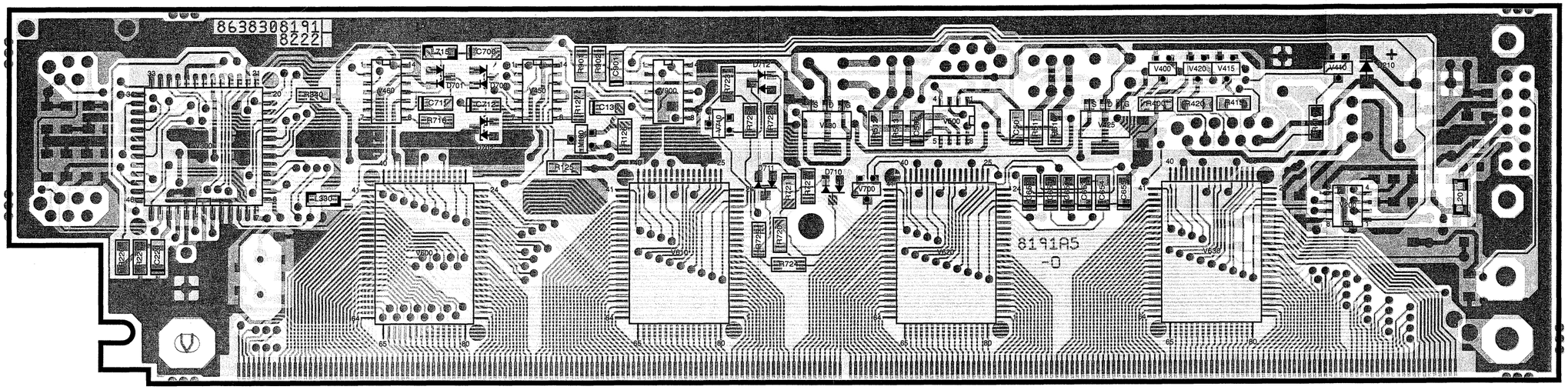
Hauptplatte
Mainboard
PL 8191 A05 V00
Chip

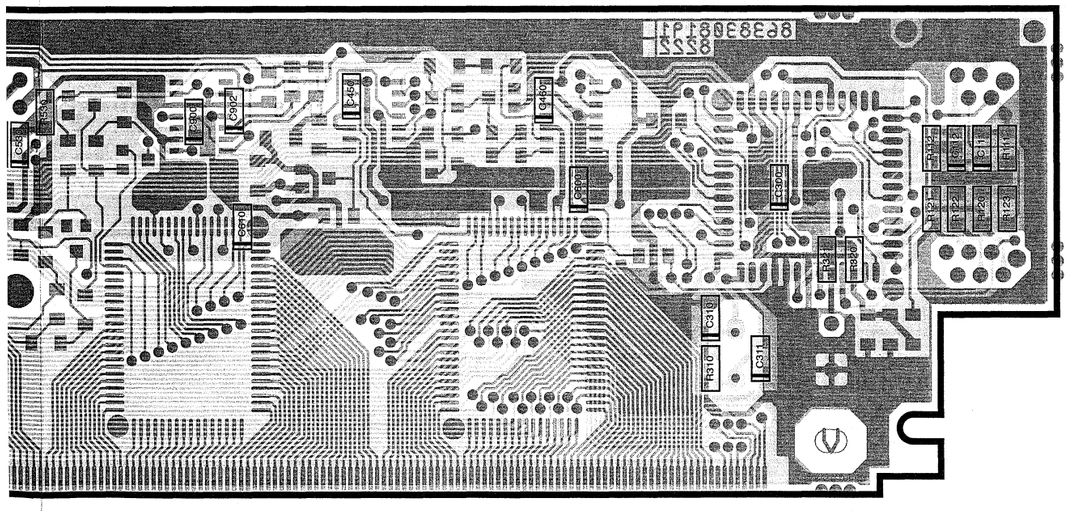


Änderun
nur mit Q

Modifica
abrégépe

Hauptplatte
Mainboard
PL 8191 A05 V00
Chip





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